

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/008,195	BRIL ET AL.	
Examiner		Art Unit		Page 1 of 1
Lewis A. Bullock, Jr.		2195		

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Nilsen, Kelvin D. et al. "Worst-Case Execution Time Analysis on Modern Processors." ACM. November 1995.
	V	Jeffay, Kevin et al. "On Non-Preemptive Scheduling of Periodic and Sporadic Tasks." IEEE. December 1991.
	W	Lee, Chang-Gun et al. "Enhanced Analysis of Cache-related Preemption Delay in Fixed-priority Preemptive Scheduling." 1997.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.